

# PATENT ABSTRACTS OF JAPAN

(11)Publication number : 2000-207202  
(43)Date of publication of application : 28.07.2000

(51)Int.CI. G06F 9/22  
G06F 9/28

(21)Application number : 11-307684 (71)Applicant : PACIFIC DESIGN KK  
(22)Date of filing : 28.10.1999 (72)Inventor : SATO TOMOMI

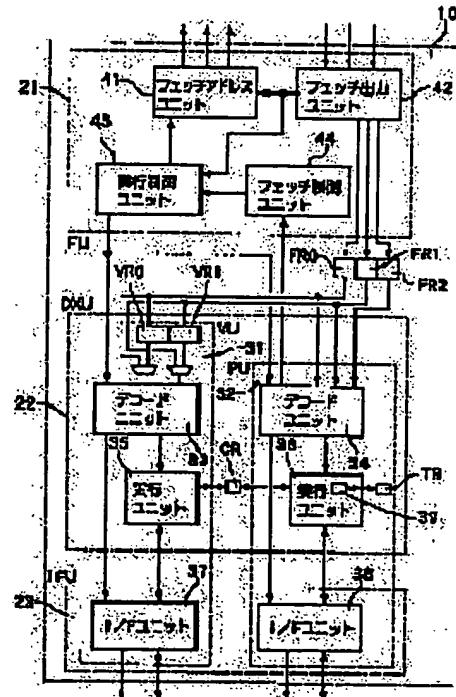
(30)Priority  
Priority number : 10308673 Priority date : 29.10.1998 Priority country : JP  
99 287565 06.04.1999 US

**(54) CONTROLLER AND DATA PROCESSOR**

(57)Abstract:

**PROBLEM TO BE SOLVED:** To provide the controller that can perform a process, which needs to be performed in real time, at a high speed and flexibly cope with alterations, extensions or the like.

**SOLUTION:** A microcode controller 10 of a microcode control system is provided with a dedicated data processing unit (VU) 31 which can perform a process using a dedicated circuit, a general data processing unit(PU) 32, and a fetch unit 21 which is common to them. This controller 10 can synchronously control the dedicated data processing unit 31 and the general data processing unit 32 with dedicated instructions and general instructions described in microcode on an instruction code level, so that multiprocessing can be performed by these units, without increasing the circuit scale. Consequently, the controller for high-speed processing that has high cost performance which includes the development cost or the like and is suitable for processing which is demanded to be processed in real time and the data processor which uses it can be provided.



## **LEGAL STATUS**

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

### Number of appeal against examiner's decision of rejection

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]